DA-3GHD-2x4

Multi Rate 3GHD-SDI Distribution Amplifier

User manual

Rev. 0

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Revision history

Current revision of this document is the uppermost in the table below.

Rev.	Repl.	Date	Sign	Change description
0	A	2011-01-31	MR	Initial version

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1 Product overview

The Flashlink DA-3GHD-2x4 is a multi bit-rate distribution amplifier module providing high performance media distribution for various signal formats from 19.4Mbps up to 2970Mbps. The unit can be configured to do cable equalizing and reclocking of SMPTE 424M, SMPTE 292M and SMPTE 259M signal formats.

The two inputs typically provide automatic cable equalizing for up to 70 meters of cable (Belden 1694A at 2970Mbps) with a total of 6 or 8 reclocked outputs. The DA-3GHD-2x4 will detect HD and SD rates and automatically switch to the correct output slew-rate.

The reclockers support the bit-rates 270Mbps, 1483.5Mbps, 1485Mbps 2967Mbps and 2970Mbps. For other rates, the reclockers automatically switch to bypass mode, and the DA-3GHD-2x4 will work as a non-reclocking distribution amplifier with cable equalizers.

The DA-3GHD-2x4 also supports reclocking of DVB-ASI at 270Mbps, enabling all possible rates including empty transport streams with only K28.5 padding packets. 4 of the outputs are non-inverting and suitable for DVB-ASI.

The unit can be configured as a 1 x 8/6 distribution amplifier. Both input ports can be used. The included intelligent change-over feature gives the ability to change input based on input signal loss, loss of lock, EDH errors or a combination of the above.

There is also included a passive bypass function (with backplane board C2 and C4) from both inputs to non-inverted outputs with less than 15 m loss of cable length (enables full redundancy in case of mains failure).

The DA-3GHD-2x4 is designed for all distribution purposes in studio, duplication and broadcast applications.

The DA-3GHD-2x4 main board can be combined with 4 different backplane boards giving functions as follows:

Product	Type of backplane board	# Outputs	GPI I/O	Relay (passive loop-through)
19240 DA-3GHD-2x4	Version C1	8	No	No
19241 DA-3GHD-2x4-PB	Version C2	8	No	Yes
19242 DA-3GHD-2x3-G	Version C3	6	Yes	No
19243 DA-3GHD-2x3-PB-G	Version C4	6	Yes	Yes

Table 1. Product overview.

Please observe that –G versions cannot be used with N-Box, Flashlink one module desktop box, due to mechanical issues.

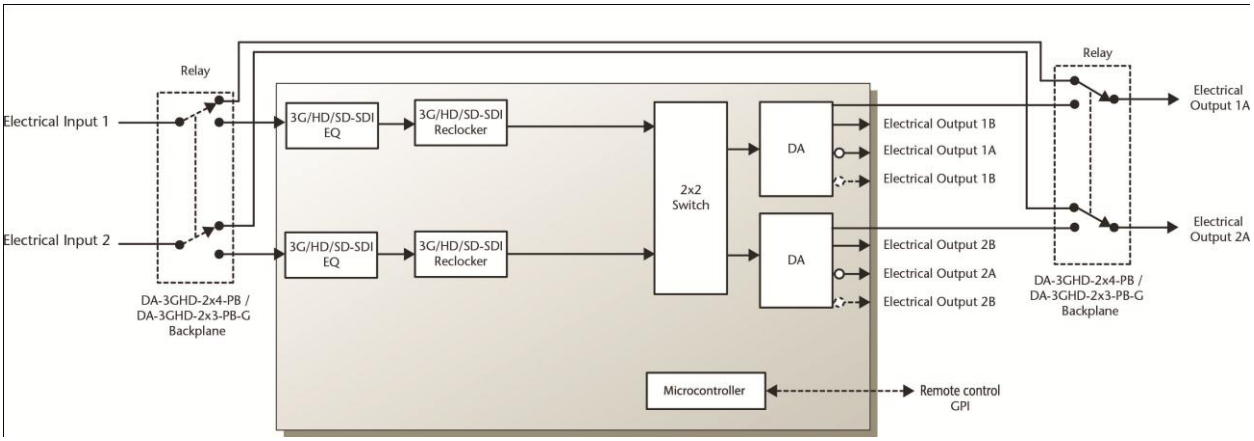

Figure 1. DA-3GHD-2x4 block diagram

2 Specifications

Electrical Outputs

Connectors	75 Ohm BNC
Output Return loss	- < -15dB, 5MHz -1.5GHz - < -10dB, 1.5GHz - 3GHz
Output signal level	800mV +/- 10%
Output signal rise / fall time 20% - 80%	- SD limit: [0.4ns – 1.5ns]; <0.5ns rise/fall var. - HD limit: < 270ps, <100ps rise/fall var. - 3G HD limit: <135ps, <50ps rise/fall var.
Amplitude overshoot	<10%
Polarity	- Output O1A, O1B, O2A and O2B: Non inverting electrical - Remaining outputs: inverting electrical
Output timing jitter	- SD: <0.2 UI - HD: <1 UI - 3G HD: <1UI
Output alignment jitter	- SD: <0.15 UI - HD: <0.15 UI - 3G HD: <0.2UI

Electrical Inputs

Connectors	75 Ohm BNC
Input Cable Eq. @270Mbps	>300m w/Belden 1694A, with BER < 10E-12
Input Cable Eq. @1485Mbps	>100m w/Belden 1694A, with BER < 10E-12
Input Cable Eq. @2970Mbps	>70m w/Belden 1694A, with BER < 10E-12
Input Return loss	- < -15dB, 5MHz -1.5GHz - < -10dB, 1.5GHz - 3GHz

Jitter tolerance	- SD limit:
	  - 10Hz-1kHz: >1 UI
	  - 10kHz – 5MHz: >0.2 UI
	- HD limit:
	  - 10Hz-100kHz: >1 UI
	  - 100kHz–10MHz: >0.2 UI
	- 3G HD limit:
	  - 10Hz-100kHz: >2 UI
	  - 100kHz–10MHz: >0.3 UI

Features

Reclocking:	Automatic SD/ HD detection Automatic output slew rate adjustment according to SMPTE 259M and SMPTE 292M / SMPTE 424M
Supported clock rates:	270, 1483.5, 1485, 2967, 2970Mbps
Input equalizers:	Eq. bypass for non-video formats or low bit rates

Supported standards

SMPTE:	SMPTE 424M, SMPTE 292M, SMPTE 259M, SMPTE 305M, SMPTE 310M
DVB-ASI:	EN50083-9 (on non-inverting outputs)

General

DC power consumption:	+5V / < 4.5W

3 Configuration

The DA-3GHD-2x4 supports a number of different formats. The correct configuration can either be set with a DIP switch or with the GYDA system controller. The layout of DA-3GHD-2x4 is shown in the drawing below with the DIP switch to the upper left position.

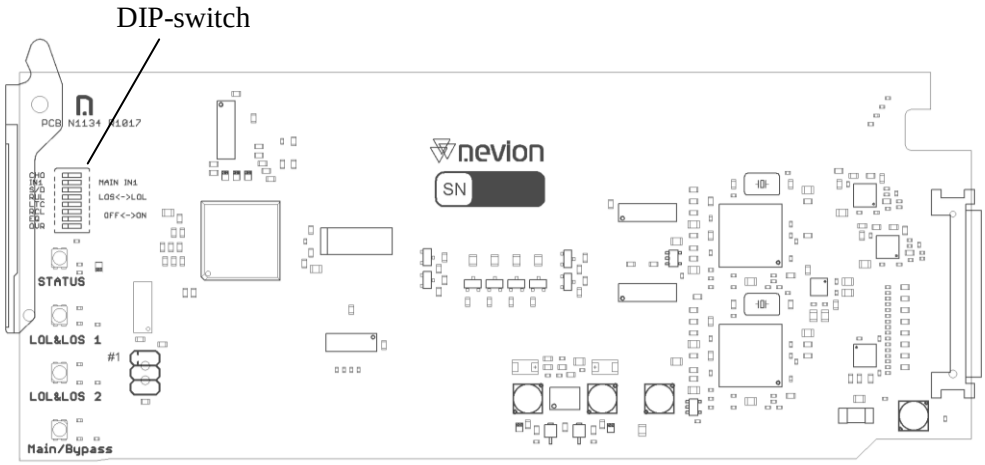

Figure 2. DA-3GHD-2x4 module DIP switches

DIP switch configuration must be set according to the table below:

Switch #	Label	Function DIP=OFF	Function DIP=ON	Comment
1	CHO			Not used
2	IN1	Input 1 stby, 2 main	Input 1 main, 2 stby	Select main/standby
3	S/D	Single distributed output	Dual distributed output	Single or dual distribution amplifier
4	RUL			Not used
5	LTC			Not used
6	RCL	Reclocker Bypass	Reclocker ON	Reclocker mode
7	EQ	Cable equalizer Bypass (Loss of signal will not work on this mode)	Cable equalizer ON	Equalizer mode
8	OVR	Module controlled by Gyda system controller	Module controlled by DIP switches	Select GYDA config. mode

Table 2. DA-3GHD DIP switches

All DIP switches are off when pointing towards the release handle.

3.1 Configuration Examples

Typical configurations for DA-3GHD-2x4:

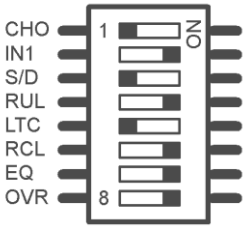

Figure 3. Distribution mode, Input 1 priority, Input 1 to all outputs, reclocker and equalizer on

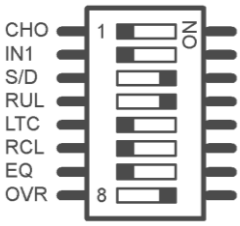

Figure 4. Distribution mode, Input 2 priority, Input 1 to 4 outputs, Input 2 to 4 outputs, transparent mode without equalization and reclocking.

4 Connections

The DA-3GHD-2x4 has four dedicated back planes: DA-3GHD-2x4-C1, - C2, - C3 and – C4. These modules are mounted at the rear of the sub-rack.
The modules are shown in figure 7 to 10.

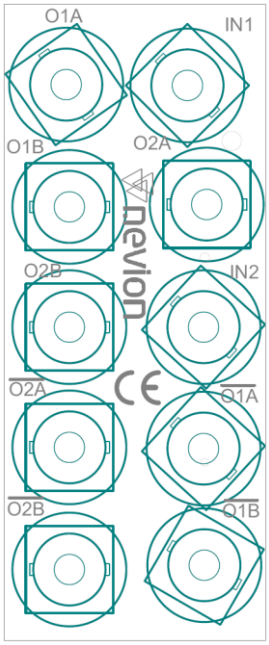

Figure 7. Overview of the DA-3GHD-2x4-C1 connector module

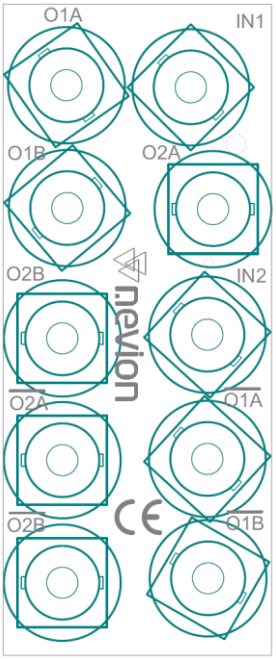

Figure 8. Overview of the DA-3GHD-2x4-C2 connector module

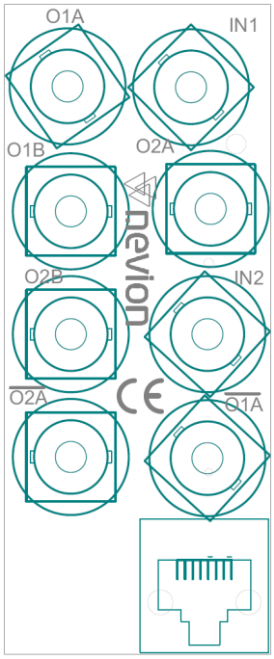

Figure 9. Overview of the DA-3GHD-2x4-C3 connector module

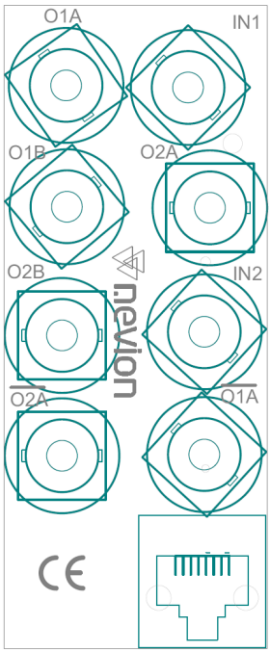

Figure 10. Overview of the DA-3GHD-2x4-C4 connector module

The electrical input signals are connected to the *IN1 and IN2* BNCs and the electrical outputs are connected to the *O*-- BNCs.

> Please note that $\overline{\text{O1A}}$, $\overline{\text{O1B}}$, $\overline{\text{O2A}}$ and $\overline{\text{O2B}}$ have inverted signals. DVB-ASI cannot be used on these outputs.
>
> Unused inputs and outputs should be terminated with 75 ohm to meet the specifications.

5 Operation

The status of the module can be easily monitored visually by the LEDs at the front of the module. The LEDs are visible through the front panel as shown in **Error! Reference source not found.** below.

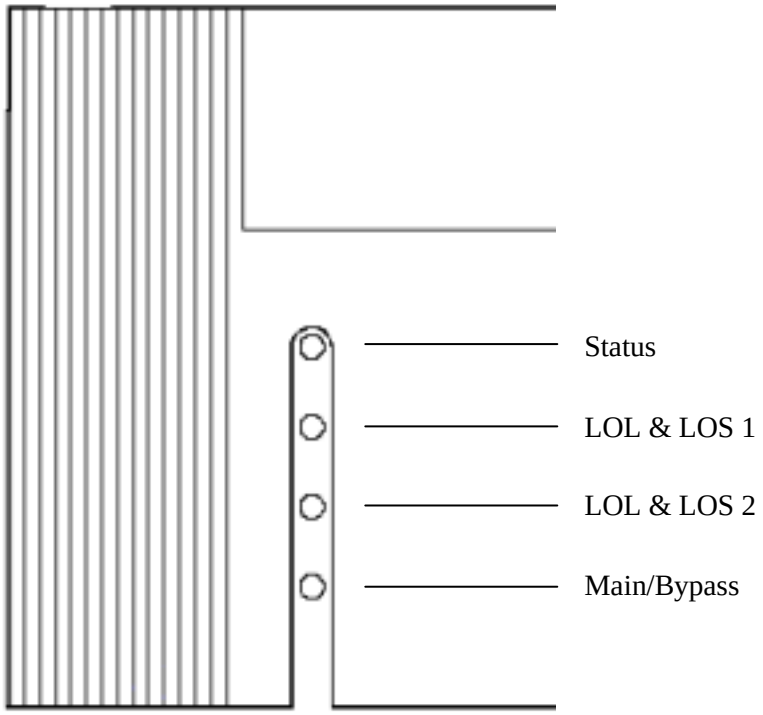

Figure 11: Panel indicator overview for (Text not printed on the front panel).

The functions of the different LEDs are described in

.

Diode \ state	Red LED	Yellow LED	Green LED	No light
Status	Module is faulty		Module is OK Module has power	Module has no power
LOL & LOS 1 (Input 1)	No Reclocker Lock & Loss Of Signal	No Reclocker lock & Signal present	Reclocker in lock & Signal present	
LOL & LOS 2 (Input 2)	No Reclocker Lock & Loss Of Signal	No Reclocker lock & Signal present	Reclocker in lock & Signal present	
Main/Bypass	No stable input	Bypass selected	Main selected	

Table 3: LED status description

GPI name (setup1/ setup2)	Function (setup1/ setup2)	Pin #	Mode	Direction	Electrical Maximums for GPI outputs: Max current: 100mA Max voltage: 30V Max power: 200mW
Status	General error status for the module.	Pin 1	Inverted Open Collector (open is alarm)	Output	
LOS[1]	Loss of signal or lock at selected input	Pin 2	Open Collector	Output	
Input 1	Input 1 selected (IN1)	Pin 3	Open Collector	Output	
Input 2	Input 2 selected (IN2)	Pin 4	Open Collector	Output	
Reset	Reset selected input to main	Pin 5	TTL, 0V = active level	Input	
Set	Set selected input to standby	Pin 6	TTL, 0V = active level	Input	
	Not used	Pin 7			
Ground	0 volt pin	Pin 8	0V.		

Table 4 - Description of GPI interface

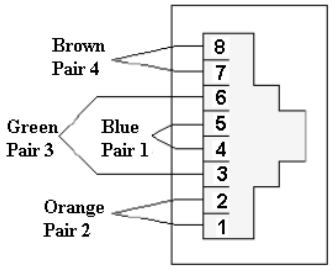

Figure 12. GPI Interface

General environmental requirements for Nevion equipment

1. The equipment will meet the guaranteed performance specification under the following environmental conditions:
   - Operating room temperature range: 0°C to 45°C
   - Operating relative humidity range: <90% (non-condensing)

2. The equipment will operate without damage under the following environmental conditions:
   - Temperature range: -10°C to 55°C
   - Relative humidity range: <95% (non-condensing)

Product Warranty

The warranty terms and conditions for the product(s) covered by this manual follow the General Sales Conditions by Nevion, which are available on the company web site:

www.nevion.com

Appendix A Materials declaration and recycling information

A.1 Materials declaration

For product sold into China after 1st March 2007, we comply with the “Administrative Measure on the Control of Pollution by Electronic Information Products”. In the first stage of this legislation, content of six hazardous materials has to be declared. The table below shows the required information.

組成名稱 Part Name	Toxic or hazardous substances and elements					
	鉛 Lead (Pb)	汞 Mercury (Hg)	镉 Cadmium (Cd)	六价铬 Hexavalent Chromium (Cr(VI))	多溴联苯 Polybrominated biphenyls (PBB)	多溴二苯醚 Polybrominated diphenyl ethers (PBDE)
DA-3GHD-2x4	O	O	O	O	O	O
O: Indicates that this toxic or hazardous substance contained in all of the homogeneous materials for this part is below the limit requirement in SJ/T11363-2006. X: Indicates that this toxic or hazardous substance contained in at least one of the homogeneous materials used for this part is above the limit requirement in SJ/T11363-2006.						

This is indicated by the product marking:

A.2 Recycling information

Nevion provides assistance to customers and recyclers through our web site http://www.nevion.com/. Please contact Nevion’s Customer Support for assistance with recycling if this site does not show the information you require.

Where it is not possible to return the product to Nevion or its agents for recycling, the following general information may be of assistance:

- Before attempting disassembly, ensure the product is completely disconnected from power and signal connections.
- All major parts are marked or labeled to show their material content.
- Depending on the date of manufacture, this product may contain lead in solder.
- Some circuit boards may contain battery-backed memory devices.

EC Declaration of Conformity

MANUFACTURER	Nevion Europe AS P.O. Box 1020, 3204 Sandefjord, Norway	
AUTHORIZED REPRESENTATIVE (Established within the EEA)	Not applicable	
MODEL NUMBER(S)	DA-3GHD-2x4	
DESCRIPTION	Multi Rate 3GHD-SDI Distribution Amplifier	
DIRECTIVES this equipment complies with	Low voltage (EU Directive 2006/95/EC) EMC (EU Directive 2004/108/EC) RoHS (EU Directive 2002/95/EC) China RoHS[1] WEEE (EU Directive 2002/96/EC) REACH	
HARMONISED STANDARDS applied in order to verify compliance with Directive(s)	EN 55103-1:2010 EN 55103-2:2010	
TEST REPORTS ISSUED BY	**Notified/Competent Body**	**Report no:**
TECHNICAL CONSTRUCTION FILE NO	Not applicable	
YEAR WHICH THE CE-MARK WAS AFFIXED	2011	
TEST AUTHORIZED SIGNATORY		
MANUFACTURER	**AUTHORIZED REPRESENTATIVE (Established within EEA)**	**Date of Issue** 2011-02-03
	Not applicable	**Place of Issue** Sandefjord, Norway
Name Thomas Øhrbom		
Position QA Director, Nevion Europe (authorized signature)		

[1] Administration on the Control of Pollution Caused by Electronic Information Products